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In the Claims:

Claims 1-22 (Cancelled)

23. (Currently amended) A mobile communication system, comprising:

a mobile antenna arranged to receive a plurality of signals from multiple signal paths from each of plural remote antennas of an external source, the plurality of signals including a first signal transmitted from a first antenna at a one time and a complement of a conjugate of a second signal transmitted from a second antenna at the one time, and including the second signal transmitted from first antenna at another time and a conjugate of the first signal transmitted from the second antenna at the another time ~~at a first time and a conjugate of a second signal transmitted at the first time;~~

~~an input circuit coupled to receive the plurality of signals from the mobile antenna, the input circuit producing a plurality of input signals including a first input signal and a second input signal, at least one of the first and at least one of the second input signals corresponding to the same datum; and~~

a correction circuit coupled to receive a plurality of estimate signals and the ~~first and second input~~ plurality of signals, the plurality of estimate signals corresponding to respective signal paths, the correction circuit producing a first symbol estimate in response to the estimate signals, the first signal, and the conjugate of the first signal, and a second symbol estimate in response to the estimate signals, the complement of the conjugate of the second signal, and the second signal ~~and the first and second input signals.~~

24. (Previously added) A mobile communication system as in claim 23, further comprising a combining circuit coupled to receive a plurality of first symbol estimates including the first symbol estimate and coupled to receive a plurality of second symbol estimates including the second symbol estimate, the combining circuit producing a first symbol signal in response to the plurality of first symbol estimates and producing a second symbol signal in response to the plurality of second symbol estimates.

25. (Currently amended) A mobile communication system as in claim 24, wherein the ~~input circuit~~, the correction circuit and the combining circuit are formed on a single integrated circuit.

26. (Previously added) A mobile communication system as in claim 24, wherein each of the first and second symbol signals include at least one of a pilot symbol, a transmit power control symbol, a rate information symbol and a data symbol.

27. (Cancelled)

28. (Previously added) A mobile communication system as in claim 23, wherein a total diversity of each of the first and second symbol signals is at least twice a number of the plural remote antennas.

29. (Currently amended) A mobile communication system as in claim 23, wherein each of the ~~first and second input~~ plurality of signals is a wideband code division multiple access signal.

30. (Previously added) A mobile communication system as in claim 29, wherein a total diversity of each of the first and second symbol signals is at least twice a number of the plural remote antennas.

31. (Currently amended) A mobile communication system as in claim 23, wherein the mobile antenna receives the ~~first and second input~~ plurality of signals over a common channel.

32. (Currently amended) A mobile communication system as in claim 23, wherein the mobile antenna receives the ~~first and second input~~ plurality of signals over a common frequency band.

33. (Cancelled)

34. (Currently amended) ~~A circuit~~ An apparatus, comprising:

a correction circuit coupled to receive a first symbol transmitted from a first antenna at a one time and a complement of a conjugate of a second symbol transmitted from a second antenna at the one time, and coupled to receive the second symbol transmitted from the first antenna at another time and a conjugate of the first symbol transmitted from the second antenna at the another time ~~transmitted symbol from a first antenna at a first time and a conjugate of a second transmitted symbol from a second antenna at the first time~~, the correction circuit producing a first symbol estimate in response to a first ~~received~~ symbol and the conjugate of a ~~second received~~ the first symbol; and

a combining circuit coupled to receive a plurality of symbol estimates including the first symbol estimate, the plurality of symbol estimates corresponding to a respective plurality of signal paths, the combining circuit producing a first symbol signal in response to the plurality of symbol estimates.

35. (Cancelled)

36. (Currently amended) ~~A circuit~~ An apparatus as in claim ~~35~~ 34, wherein the correction circuit produces ~~the first symbol estimate and a second symbol estimate in response to the first transmitted symbol, the conjugate of the second transmitted symbol, a second transmitted symbol, and the complement of a conjugate of the first transmitted symbol~~ second symbol and the complement of the conjugate of the second symbol.

37. (Currently amended) ~~A circuit~~ An apparatus as in claim 36, wherein the correction circuit is further coupled to receive a first estimate signal and a second estimate signal and wherein the correction circuit produces the first symbol estimate and the second symbol estimate in response to the first ~~transmitted~~ symbol, the conjugate of the ~~second transmitted~~ first symbol, the second ~~transmitted~~ symbol, the complement of the conjugate of the ~~first transmitted~~ second symbol, the first estimate signal, and the second estimate signal.

38. (Currently amended) A circuit ~~An apparatus~~ as in claim 34, wherein the correction circuit receives the first symbol and the complement of the conjugate of the second symbol over a common channel.

39. (Currently amended) A circuit ~~An apparatus~~ as in claim 34, wherein the correction circuit receives the first symbol and the complement of the conjugate of the second symbol over a common frequency band.

40. (Currently amended) A circuit ~~An apparatus~~ as in claim 34, wherein the plurality of symbol estimates corresponds to one of the first and second symbols.

41. (Currently amended) A circuit ~~An apparatus~~ as in claim 34, wherein the combining circuit is a rake combiner.

42. (Currently amended) A method of processing signals in a communication circuit, comprising the steps of:

receiving a first ~~transmitted~~ symbol transmitted from a first antenna at a ~~first~~ one time and a complement of a conjugate of a second transmitted symbol transmitted from a second antenna at the ~~first~~ one time;

receiving the second symbol from the first antenna at another time and a conjugate of the first symbol from the second antenna at the another time;

producing a first symbol estimate in response to ~~a first received~~ the first symbol and a conjugate of ~~a second received~~ the first symbol;

receiving a plurality of symbol estimates including the first symbol estimate, the plurality of symbol estimates corresponding to a respective plurality of signal paths; and

producing a first symbol signal in response to the plurality of symbol estimates.

43. (Cancelled)

44. (Currently amended) The method of claim 43 42, further comprising the step of producing ~~the first symbol estimate and~~ a second symbol estimate in response to the ~~first symbol,~~ the conjugate of the ~~second symbol,~~ the second symbol, and the complement of the conjugate of the ~~first~~ second symbol.

45. (Currently amended) The method of claim 43, further comprising the step of receiving a first estimate signal and a second estimate signal and producing the first symbol estimate and the second symbol estimate in response to the first symbol, the conjugate of the ~~second~~ first symbol, the second symbol, the complement of the conjugate of the ~~first~~ second symbol, the first estimate signal, and the second estimate signal.

46. (Currently amended) The method of claim 42, wherein the first symbol and the complement of the conjugate of the second symbol are received over a common channel.

47. (Currently amended) The method of claim 42, wherein the first symbol and the complement of the conjugate of the second symbol are received over a common frequency band.

48. (Previously added) The method of claim 42, wherein the plurality of symbol estimates corresponds to one of the first and second symbols.

49. (Previously added) The method of claim 42, wherein the combining circuit is a rake combiner.

50. (Currently amended) An apparatus, comprising:

a correction circuit coupled to receive a first ~~symbol~~ signal comprising one symbol transmitted from a first antenna at a first one time and a complement of a conjugate of another symbol transmitted from a second antenna at the first one time, and coupled to receive a second signal comprising the another symbol transmitted from the first antenna at another time and a conjugate of the one symbol transmitted from the second antenna at the another time, the

correction circuit producing a first symbol estimate in response to the ~~first one~~ one symbol and the conjugate of a ~~second~~ the one symbol; and

a combining circuit coupled to receive a plurality of symbol estimates including the first symbol estimate, the plurality of symbol estimates corresponding to a respective plurality of signal paths, the combining circuit producing a combined symbol in response to the plurality of symbol estimates.

51. (Cancelled)

52. (Currently amended) An apparatus as in claim ~~51~~ 50, wherein the correction circuit produces ~~the first symbol estimate and a second symbol estimate in response to receiving said one symbol, said conjugate of said another symbol, yet another symbol, and a complement of a conjugate of said one symbol~~ the another symbol and a complement of a conjugate of the another symbol.

53. (Currently amended) An apparatus as in claim 50, wherein the correction circuit is further coupled to receive a first estimate signal and a second estimate signal and wherein the correction circuit produces the first symbol estimate and the second symbol estimate in response to said one symbol, the complement of the conjugate of the second transmitted another symbol, said another symbol, ~~the complement of the conjugate of said one symbol~~, the first estimate signal, and the second estimate signal.

54. (Currently amended) An apparatus as in claim 50, wherein the correction circuit receives the ~~first one~~ one symbol and the complement of the conjugate of the second another symbol over a common channel.

55. (Currently amended) An apparatus as in claim 50, wherein the correction circuit receives the ~~first one~~ one symbol and the complement of the conjugate of the second another symbol over a common frequency band.

56. (Currently amended) An apparatus as in claim 50, wherein the plurality of symbol estimates corresponds to one of the ~~first and second~~ one and the another symbols.

57. (Previously added) An apparatus as in claim 50, wherein the combining circuit is a rake combiner.

58. (Currently amended) A method of processing signals in a communication circuit, comprising the steps of:

receiving a first ~~symbol~~ signal comprising a first symbol from a first antenna at a first one time and a complement of a conjugate of ~~another~~ a second symbol from a second antenna at the first one time;

receiving a second signal comprising the second symbol from the first antenna at another time and a conjugate of the first symbol from the second antenna at the another time;

producing a first symbol estimate in response to the first symbol and ~~a conjugate of a second~~ the conjugate of the first symbol;

receiving a plurality of symbol estimates including the first symbol estimate, the plurality of symbol estimates corresponding to a respective plurality of signal paths; and

producing a combined symbol in response to the plurality of symbol estimates.

59. (Cancelled)

60. (Currently amended) The method of claim ~~59~~ 58, further comprising the step of producing ~~the first symbol estimate and a second symbol estimate in response to the first symbol, the conjugate of the second symbol, the second symbol, the second symbol~~ the second symbol and the complement of the conjugate of the first second symbol.

61. (Currently amended) The method of claim 59, further comprising the step of receiving a first estimate signal and a second estimate signal and producing the first symbol estimate and the second symbol estimate in response to the first symbol, the complement of the conjugate of the

second symbol, the second symbol, the complement of the ~~conjugate of the~~ first symbol, the first estimate signal, and the second estimate signal.

62. (Currently amended) The method of claim 58, wherein the first symbol and the complement of the conjugate of the second symbol are received over a common channel.

63. (Currently amended) The method of claim 58, wherein the first symbol and the complement of the conjugate of the second symbol are received over a common frequency band.

64. (Previously added) The method of claim 58, wherein the plurality of symbol estimates corresponds to one of the first and second symbols.

65. (Previously added) The method of claim 58, wherein the combining circuit is a rake combiner.